

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete if Known	
Sheet		1	of	2	Application / Conf. No. Unknown <u>101 684183</u>
					Filing Date October 10, 2003
					First Named Inventor Trevor J. Bauer
					Art Unit Unknown
					Examiner Name Unknown
					Attorney Docket Number X-1412 US

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No.¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
d3		US- 5,914,616	06-22-99	Young et al.	
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FOREIGN PATENT DOCUMENTS							
Examiner Initials *	Cite No.¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T *
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Examiner Signature		Date Considered	<u>4/7/05</u>
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¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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Sheet	2	of	2	

OTHER -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
<u>GA</u>		Xilinx, Inc.; "The Programmable Logic Data Book 2000"; published April 2000; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp 3-75 through 3-96.	
		Xilinx, Inc.; "Virtex-II Platform FPGA Handbook"; published December 2000; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp 33-75.	
		Xilinx, Inc.; "Virtex-II Pro Platform FPGA Handbook"; published October 14, 2002; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp 3-75 through 19-71.	
<u>W</u>		Neil H.E. Weste and Kamran Eshraghian; "Principles of CMOS VLSI Design - A Systems Perspective"; Chapter 7; Second Edition; published by Addison-Wesley; Copyright 1993; pp. 466-510. <u>No mention</u>	

Examiner Signature	<u>D & W</u>	Date Considered	<u>4/7/05</u>
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